

Department of Electronics & Communication Engineering
RAM KRISHNA MAHATO GOVERNMENT ENGINEERING COLLEGE PURULIA

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CLASS ROUTINE FOR ODD SEMESTER 2018
B. TECH (ECE) 2ND YEAR 3RD SEMESTER, 2018 (ROOM NO:)

Day / Time	09.40 - 10.35	10.35 - 11.30	11.30 - 12.25	12.25 - 01.20	01.20 - 02.00	02.00 - 02.55	02.55 - 03.50	03.50 - 04.45	04.45 - 05.40	
Monday	EC304 [2L]		M302 [2L*]		B R E A K	M(CS)301 [T]	M(CS)391 (GR-A) / EC394 (GR-B)			
Tuesday	EC302 [2L]					M302 [T*]	EC391 (GR-A)/ EC393 (GR-B)			
Wednesday	EC301 [2L]		EC304 [L]	EC304 [T]			EC393 (GR-A)/ EC392 (GR-B)			
Thursday	MCS301 [2L]		EC301 [L]	EC301 [T]		EC303 [L]	EC394 (GR-A)/ M(CS)391 (GR-B)			
Friday	EC302 [L]	EC303 [2L]		M302 [L**]			EC392 (GR-A)/EC391 (GR-B)			
Saturday	REMEDIAL AND MENTORING CLASSES						REMEDIAL AND MENTORING CLASSES			
Sl. No	Paper Code	Total Number of Contact Hours			Credits	Paper Name	Faculty By Name			
		L	T	P						
THEORY										
1	M(CS)301	2	1	0	2	Numerical Methods	Dr. Abdur Rahaman Sardar			
2	M302	3	1	0	4	Mathematics-III	Mr. Dipak Kumar Jana*/ Mr. Saroj Kumar Mondal**			
3	EC301	3	1	0	4	Circuit Theory & Networks	Mr. Dipankar Chatterjee			
4	EC302	3	0	0	3	Solid State Device	Mr. Hiranmay Mistry			
5	EC303	3	0	0	3	Signals & Systems	Dr. Tirtha Sankar Das			
6	EC304	3	1	0	4	Analog Electronic Circuits	Mr. Santigopal Maity			
PRACTICAL										
1	M(CS) 391	0	0	2	1	Numerical Lab	Dr. Abdur Rahaman Sardar			
2	EC391	0	0	3	2	Circuit Theory & Network Lab	Mr. Dipankar Chatterjee			
3	EC392	0	0	3	2	Solid State Devices	Mr. Hiranmay Mistry			
4	EC393	0	0	3	2	Signals & Systems	Dr. Tirtha Sankar Das			
5	EC394	0	0	3	2	Analog Electronic Circuits	Mr. Santigopal Maity			